

Fig. 1 PRIOR ART

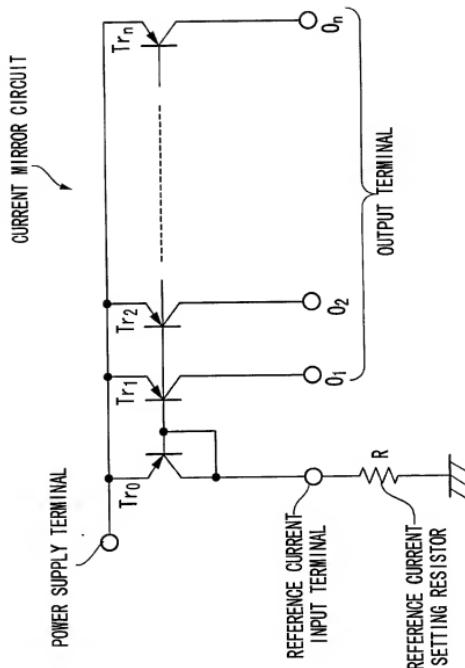


Fig. 2 PRIOR ART

DEVIATION IN CURRENT RATION AND WIRING RESISTANCE OF CURRENT MIRROR CIRCUIT

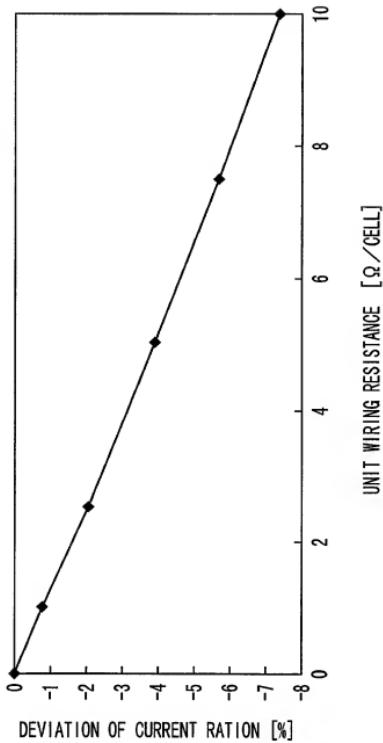


Fig. 3A PRIOR ART

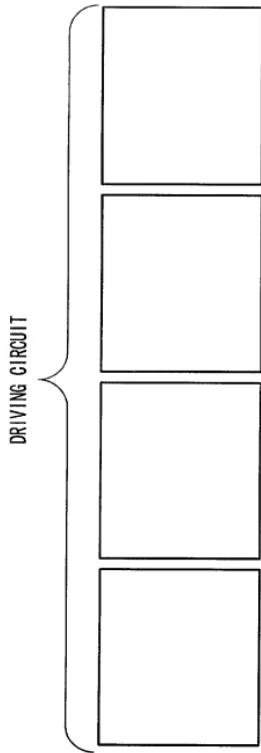


Fig. 3B PRIOR ART

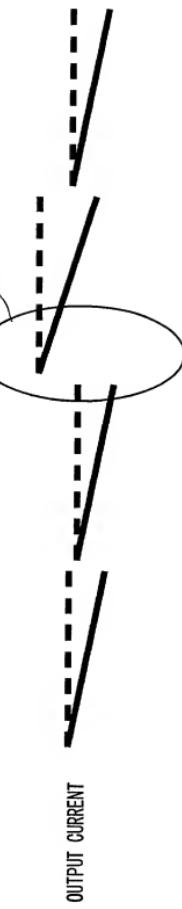


Fig. 4

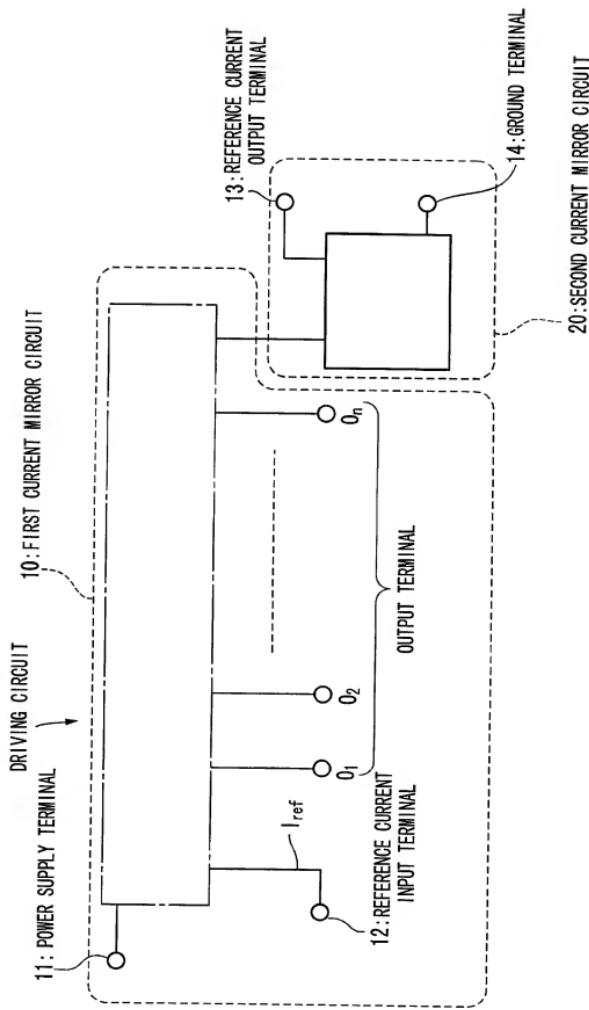
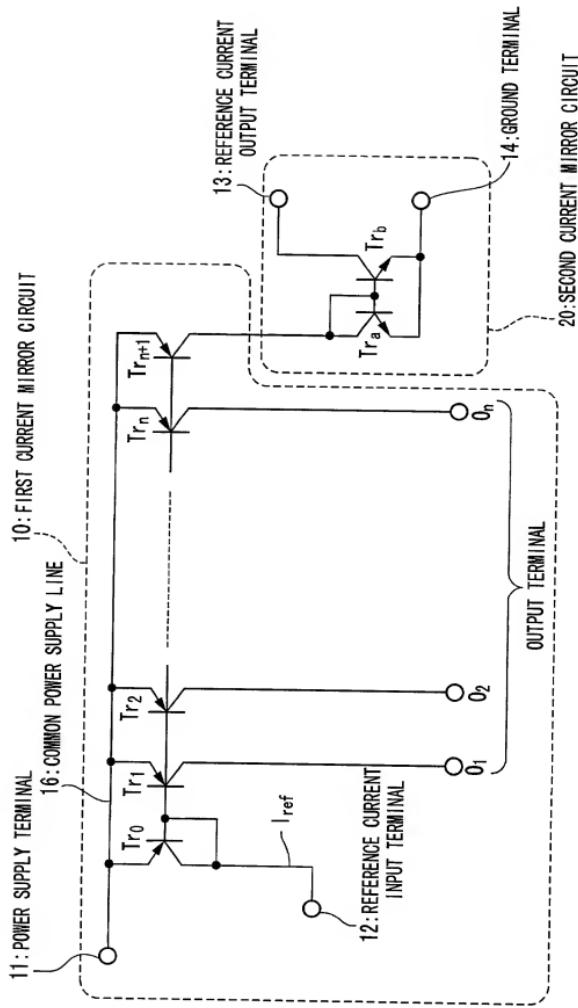


Fig. 5



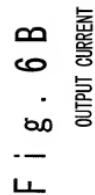
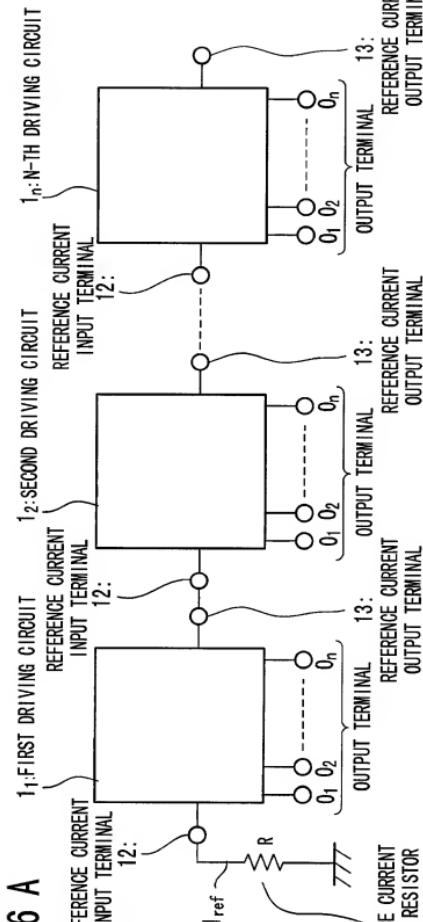


Fig. 7

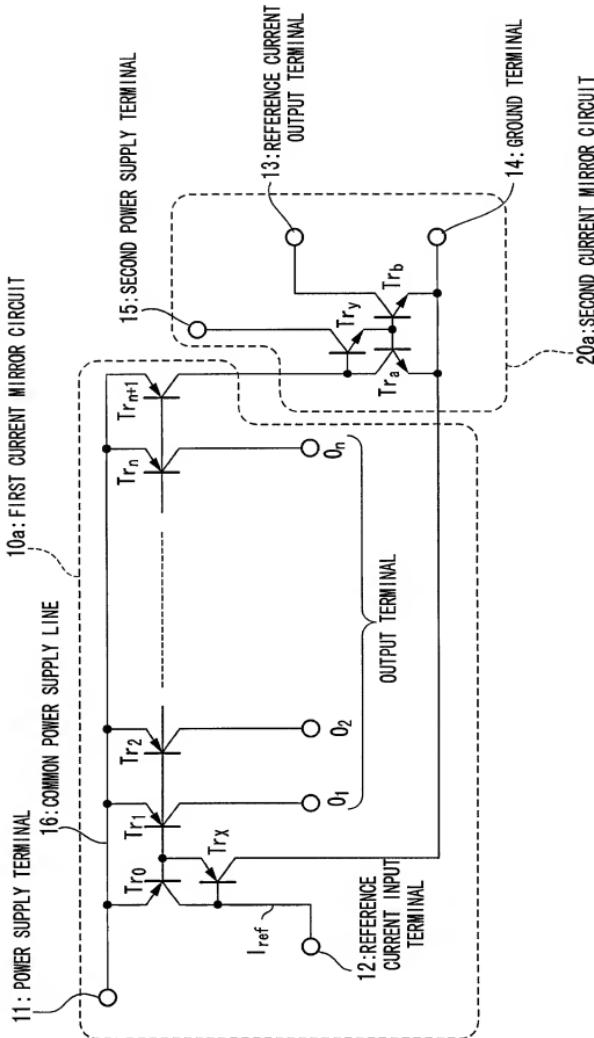


Fig. 8

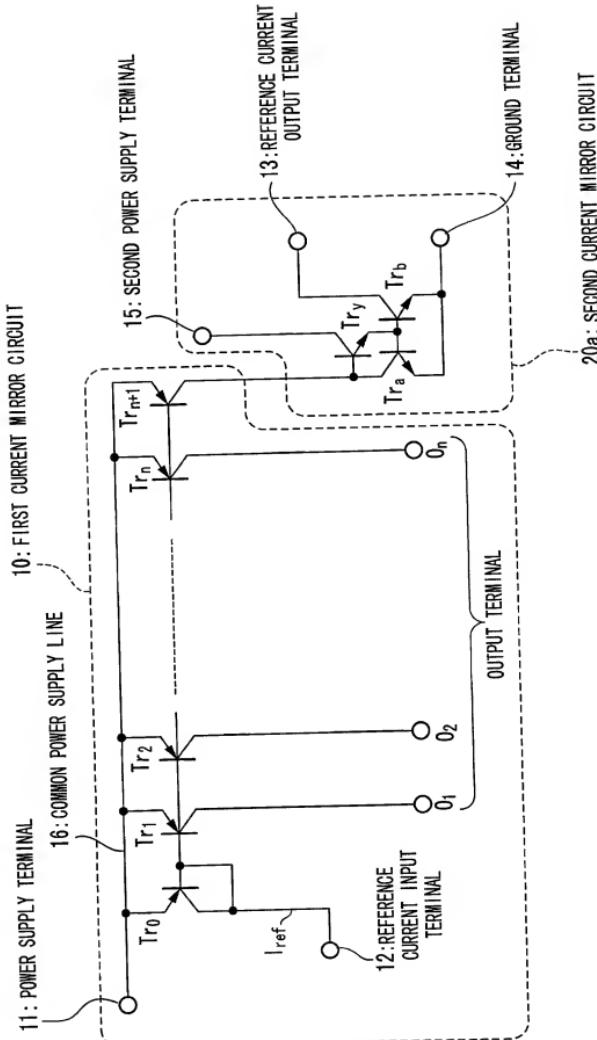


Fig. 9

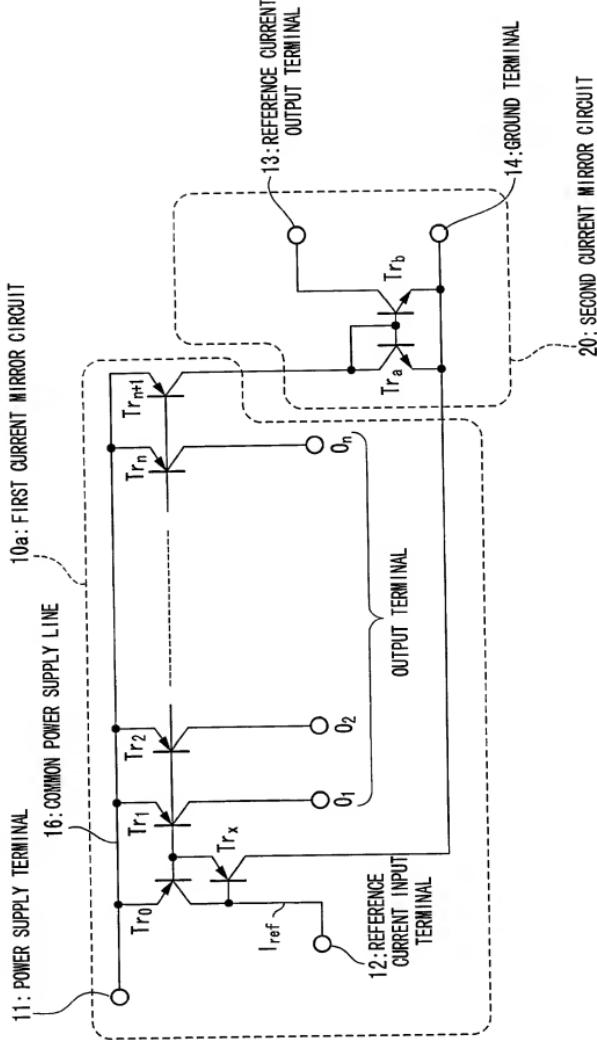


Fig. 10

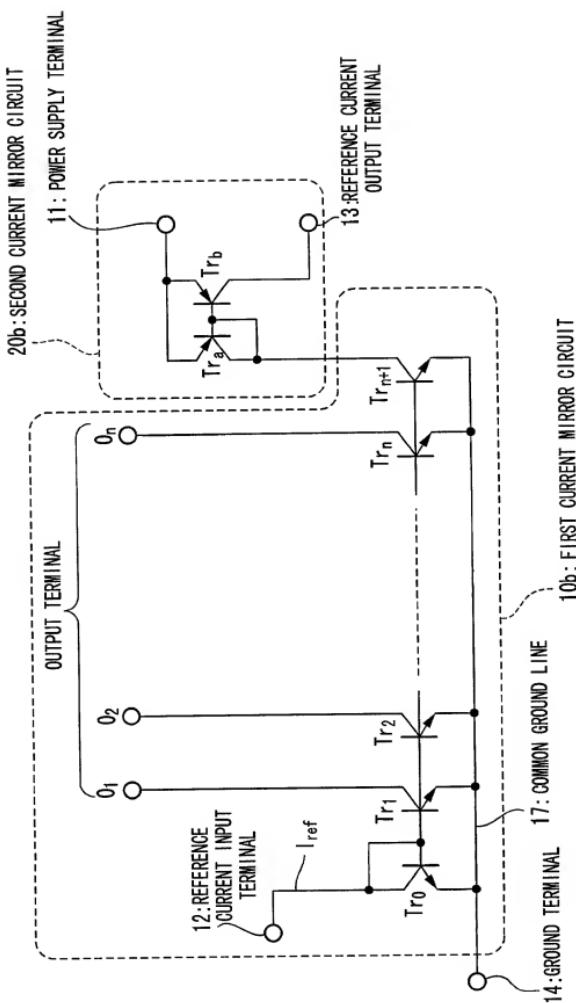


Fig. 11

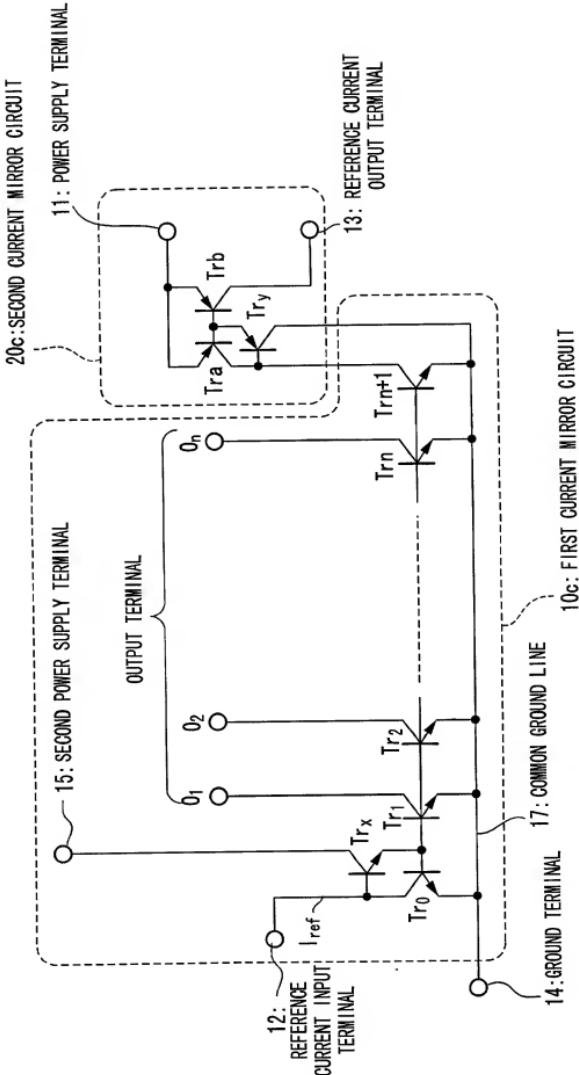


Fig. 12

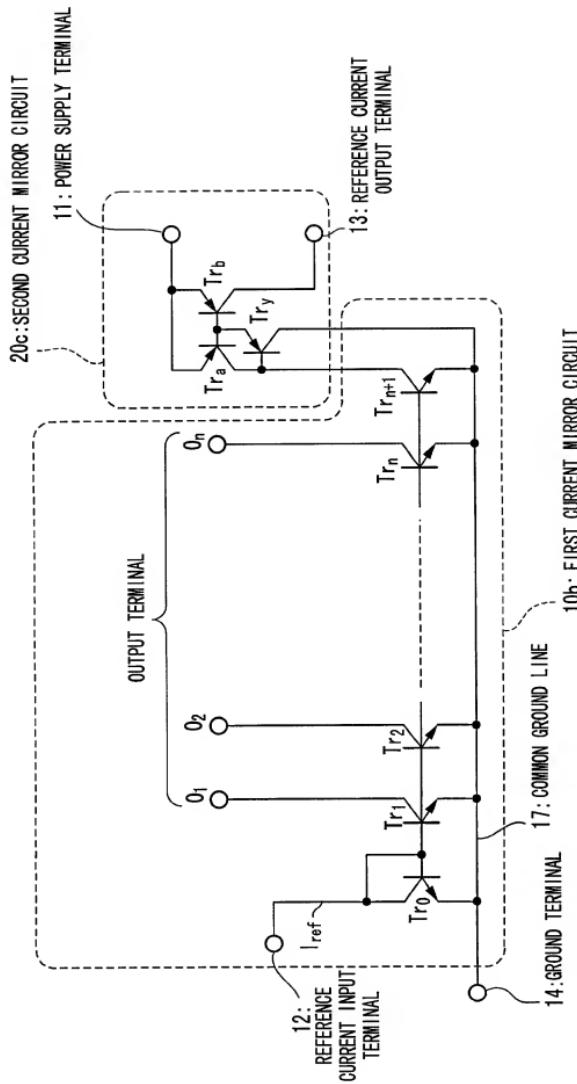


Fig. 13

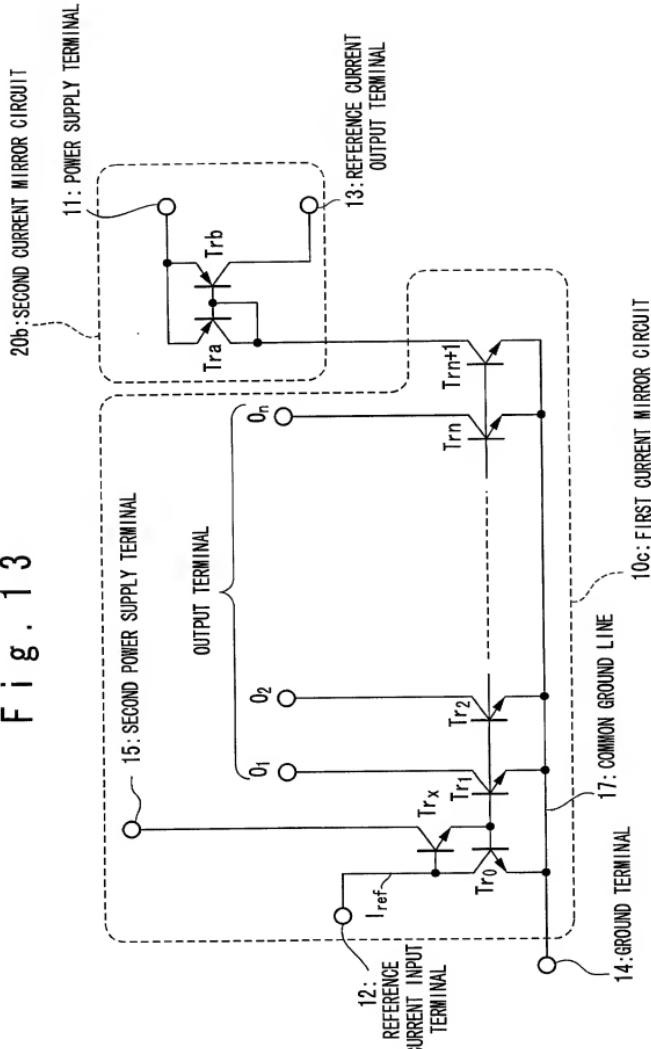


Fig. 14

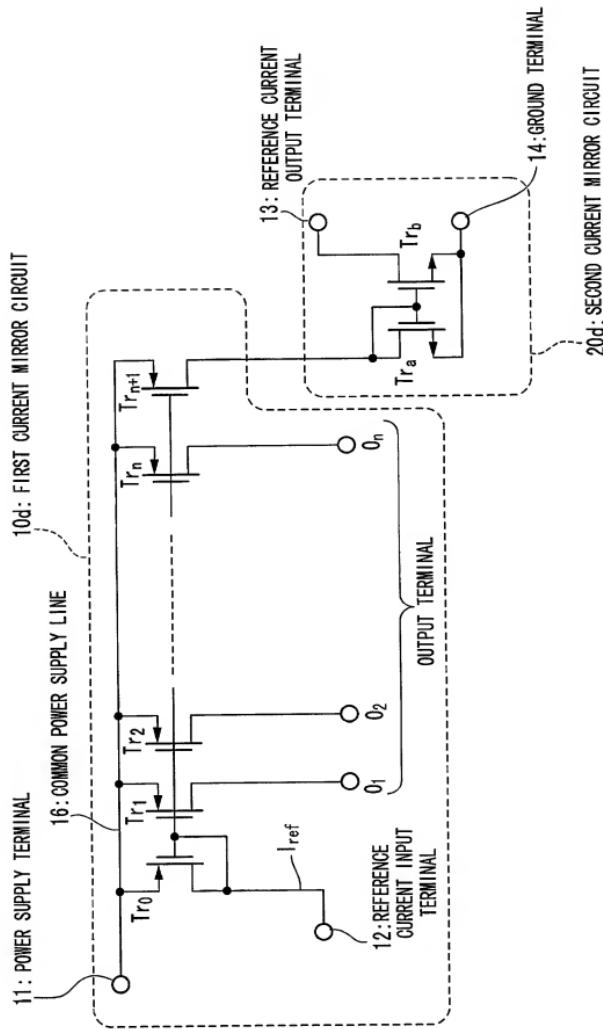


Fig. 15

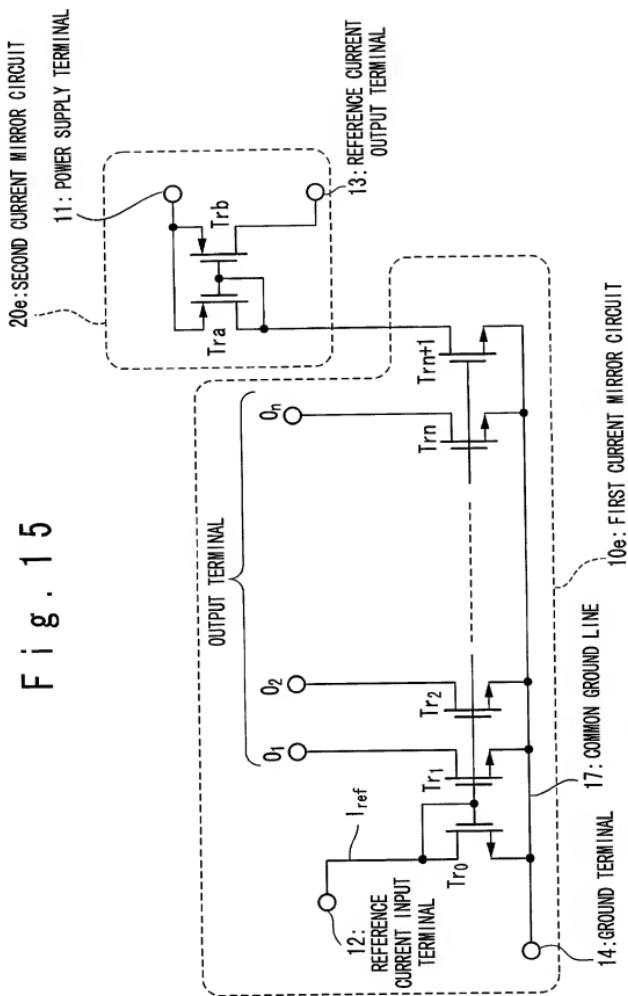


Fig. 16

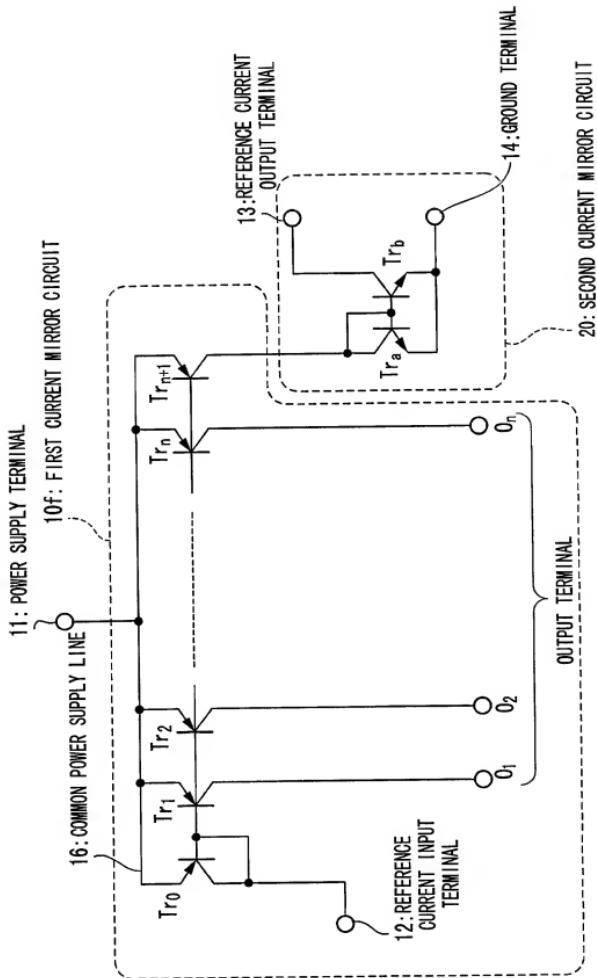


Fig. 17

